

Bachelor's Thesis (taken)

Determining the Pareto frontier of Error Bound Combinations

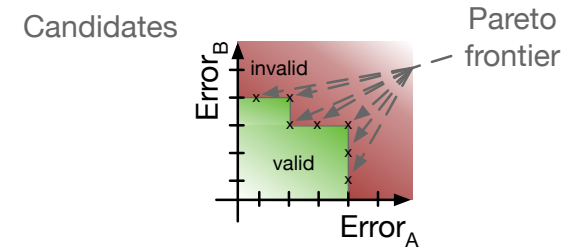
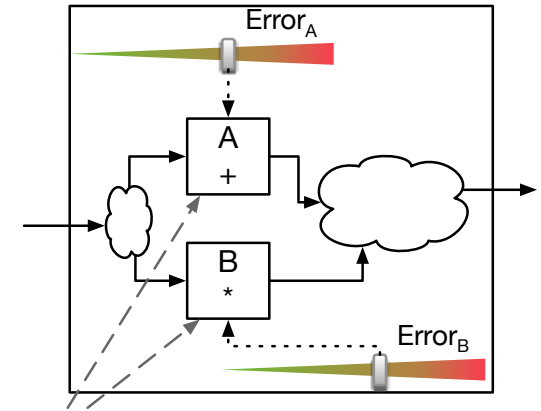
Approximate Computing allows the introduction of errors into a circuit to improve a target metric, e.g., area. Circuit modules subject to approximations are called *candidates*. A key challenge is to find candidate combinations which optimize the target metric while satisfying given quality constraints. The thesis' goal is to modify an existing verification flow to evaluate different error bound combinations of candidates to determine the Pareto frontier.

Type of project

- Familiarization with existing approximation and verification flows
- Implementation of verifying candidate combinations & determining the Pareto frontier
- Setting up and evaluating experiments

Prerequisites

- Programming skills in Python, C/C++, and VHDL/Verilog (or will to learn)
- (Basic) knowledge in circuit design and verification helpful



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