

Bachelor's Thesis

Power Analysis of Embedded FPGA Accelerators

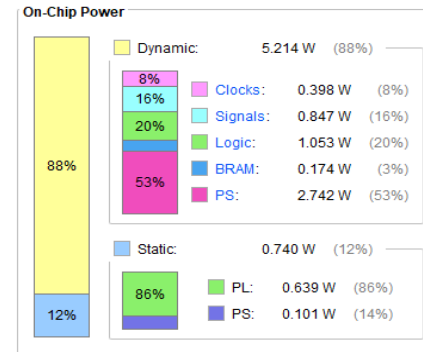
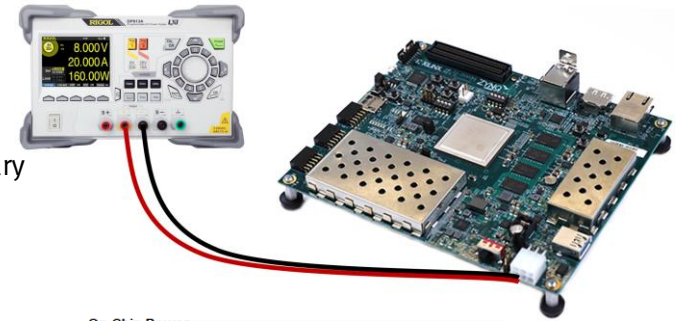
Measuring and estimating the power consumption of FPGA-based accelerators is necessary for benchmarking and optimization purposes. This project aims to build a measurement tool for heterogenous System-on-Chip platforms, which can be used for automated tests and real-time visualization (Jupyter). The developed tool will be used for existing FPGA designs to analyze and evaluate the accuracy of power estimates and to provide insight into the relationship between power draw and tunable accelerator parameters, such as frequency, performance, resource selection, etc.

Type of project

- Developing a software tool
- Conducting experiments

Prerequisites

- Programming skills (Python)
- Experience with Xilinx tools is helpful



Supervisor

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